## Features

－Solid－state silicon－avalanche technology
－ 350 Watts Peak Pulse Power per Line $\left(t_{p}=8 / 20 \mu s\right)$
－Low operating and clamping voltages
－Up to Four（4）Lines of Protection
－Working Voltage： 5 V
－Low Leakage

## IEC Compatibility（EN61000－4）


－IEC 61000－4－2（ESD）$\pm 15 \mathrm{kV}$（air），$\pm 8 \mathrm{kV}$（contact）
－IEC 61000－4－4（EFT）40A（5／50ns）
－IEC 61000－4－5（Lightning）24A（8／20 $\mu \mathrm{s}$ ）

## Mechanical Characteristics

－SOT－23－6L package
－Molding compound flammability rating： UL 94V－0
－Marking：Marking Code
－Packaging：Tape and Reel
－RoHS Compliant

## Circuit Diagram



## Applications

－Cell phone Handsets \＆Accessories
－Personal Digital Assistants（PDAs）
－Notebook，Laptop，and Palmtop Computers
－Portable Instrumentation
－Digital Cameras
－MP3 Player

## Schematic \＆PIN Configuration



SOT－23－6L（Top View）

## Absolute Maximum Rating

| Rating | Symbol | Value | Units |
| :--- | :---: | :---: | :---: |
| Peak Pulse Power $\left(t_{p}=8 / 20 \mu \mathrm{~s}\right)$ | $\mathrm{P}_{\mathrm{PP}}$ | 350 | Watts |
| Peak Forward Voltage $\left(\mathrm{I}_{\mathrm{F}}=1 \mathrm{~A}, \mathrm{t}_{\mathrm{p}}=8 / 20 \mu \mathrm{~s}\right)$ | $\mathrm{V}_{\mathrm{FP}}$ | 1.35 | V |
| Operating Temperature | $\mathrm{T}_{J}$ | -55 to +125 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | $\mathrm{T}_{\text {STG }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

## Electrical Parameters $\left(\mathbf{T}=\mathbf{2 5}{ }^{\circ} \mathrm{C}\right)$

| Symbol | Parameter |
| :---: | :--- |
| $\mathrm{I}_{\mathrm{PP}}$ | Maximum Reverse Peak Pulse Current |
| $\mathrm{V}_{\mathrm{C}}$ | Clamping Voltage＠IPP |
| $\mathrm{V}_{\mathrm{RWM}}$ | Working Peak Reverse Voltage |
| $\mathrm{I}_{\mathrm{R}}$ | Maximum Reverse Leakage Current＠ $\mathrm{V}_{\mathrm{RWM}}$ |
| $\mathrm{V}_{\mathrm{BR}}$ | Breakdown Voltage＠ $\mathrm{IT}_{\mathrm{T}}$ |
| $\mathrm{I}_{\mathrm{T}}$ | Test Current |
| $\mathrm{I}_{\mathrm{F}}$ | Forward Current |
| $\mathrm{V}_{\mathrm{F}}$ | Forward Voltage＠ $\mathrm{I}_{\mathrm{F}}$ |



## Electrical Characteristics

## DW05MS－S

| Parameter | Symbol | Conditions | Minimum | Typical | Maximum | Units |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse Stand－Off Voltage | $\mathrm{V}_{\mathrm{RWM}}$ |  |  |  | 5.0 | V |
| Reverse Breakdown Voltage | $\mathrm{V}_{\mathrm{BR}}$ | $\mathrm{I}=1 \mathrm{~mA}$ | 6.0 |  |  | V |
| Reverse Leakage Current | $\mathrm{I}_{\mathrm{R}}$ | $\mathrm{V}_{\mathrm{RWm}}=5 \mathrm{~V}, \mathrm{~T}=25^{\circ} \mathrm{C}$ |  |  | 1 | $\mu \mathrm{~A}$ |
| Peak Pulse Current | $\mathrm{I}_{\mathrm{PP}}$ | $\mathrm{t}_{\mathrm{p}}=8 / 20 \mu \mathrm{~s}$ |  |  | 24 | A |
| Clamping Voltage | $\mathrm{V}_{\mathrm{C}}$ | $\mathrm{I}_{\mathrm{PP}}=5 \mathrm{~A}, \mathrm{t}_{\mathrm{p}}=8 / 20 \mu \mathrm{~s}$ |  |  | 9.5 | V |
| Clamping Voltage | $\mathrm{V}_{\mathrm{C}}$ | $\mathrm{I}_{\mathrm{PP}}=24 \mathrm{~A}, \mathrm{t}_{\mathrm{p}}=8 / 20 \mu \mathrm{~s}$ |  | 13.5 | 15 | V |
| Junction Capacitance | $\mathrm{C}_{\mathrm{j}}$ | Between <br> Ground <br> $\mathrm{V}_{\mathrm{R}}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  |  |  |  |

## Typical Characteristics

Figure 1：Peak Pulse Power vs．Pulse Time


Figure 3：Clamping Voltage vs．Peak Pulse Current


Figure 5：Normalized Junction Capacitance vs．Reverse Voltage


Figure 2：Power Derating Curve


Figure 4：WE05MF Insertion Loss


Figure 6：ESD Pulse Waveform（Per IEC 61000－4－2）


Refl 500 mV 10．0ns

## Application Information

The DWxxMS－S Series are TVS arrays designed to protect I／O or data lines from the damaging effects of ESD or EFT．This product provides unidirectional protection；the device is connected as follows：

## UNIDIRECTIONAL COMMON－MODE CONFIGURATION

The DWxxMS－S Series provides up to four（4）lines of protection in a common－mode configuration as depicted in Figure 7．Circuit connectivity is as follows：
－I／O 1 is connected to Pin 1.
－I／O 2 is connected to Pin 3.
－I／O 3 is connected to Pin 4.
－I／O 4 is connected to Pin 6.
－Pin 2 is connected to ground．
－Pin 5 is not connected


Figure 7 Unidirectional Configuration Common－Mode I／O Port Protections

## CIRCUIT BOARD LAYOUT RECOMMENDATIONS

Circuit board layout is critical for Electromagnetic Compatibility（EMC）protection．The following guidelines are recommended：
－The protection device should be placed near the input terminals or connectors，the device will divert the transient current immediately before it can be coupled into the nearby traces．
－The path length between the TVS device and the protected line should be minimized．
－All conductive loops including power and ground loops should be minimized．
－The transient current return path to ground should be kept as short as possible to reduce parasitic inductance．
－Ground planes should be used whenever possible．For multilayer PCBs，use ground vias．

## Outline Drawing－SOT－23－6



## Marking Codes

| Part Number | DW05MS－S |
| :--- | :---: |
| Marking Code | 05 S |

## Package Information

Qty：3k／Reel

